



#5/A  
2859  
7-10-02  
M.L.

Atty. Docket No.: 3167-Z

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of  
Laung-Terng Wang et al.  
Serial No. 10/067,372

Filed: February 7, 2002

Group Art Unit 2859

For: MULTIPLE-CAPTURE DFT SYSTEM FOR DETECTING OR  
LOCATING CROSSING CLOCK-DOMAIN FAULTS DURING  
SELF-TEST OR SCAN-TEST

TECHNOLOGY CENTER 2800

JUN -7 2002

RECEIVED

PRELIMINARY AMENDMENT

Hon. Commissioner of Patents and Trademarks  
Washington, D.C. 20231

RECEIVED

JUL 05 2002

Sir:

Please amend the above-identified application before  
examination on the merits as follows:

IN THE SPECIFICATION:

Amend the paragraph bridging pages 4 and 5 to read as follows:

In using the one-hot DFT technique, each crossing clock-domain signal flowing into its receiving clock domains must be initialized to or held at a predetermined logic value of 0 or 1 first. This initialization is usually accomplished by shifting in predetermined logic values to all clock domains so that all crossing clock-domain signals are forced to a known state. Testing is then conducted domain-by-domain, thus, called one-hot testing. See U.S. Pat. No. 5,680,543 issued to Bhawmik et al. (1997). The major benefits of using this approach are that it can still detect or locate crossing clock-domain

A1  
cm't